

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Hiroyuki Yoshida                      ATTY DOCKET NO. 09792909-6113  
SERIAL NO. 10/524,749                      GROUP ART UNIT: 2814  
FILING DATE: February 15, 2005                      EXAMINER: Abul Kalam  
INVENTION: "INSULATED GATE FIELD EFFECT TRANSISTOR AND METHOD  
OF MANUFACTURING SAME, AND IMAGE PICKUP DEVICE AND  
METHOD OF MANUFACTURING SAME"

**"RESPONSE TO ELECTION/RESTRICTION REQUIREMENT"**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

S I R:

This amendment is being filed in response to the Office communication dated April 13, 2006. Please reconsider the application in view of the amendment and remarks presented below.

**ELECTION OF INVENTION**

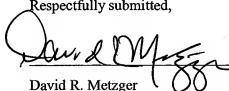
In response to the restriction requirement, applicants hereby designates Group II, claims 4-5 and 7 for examination.

Applicant reserves the right to file divisional applications covering the subject matter of the non-elected claims.

Respectfully submitted,

Date:

May 12, 2006



, Reg. 32,919

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